

UN485CUA

Features

- ◆ 5V Power Supply, Half-duplex;
- allows up to 32 transceivers on the bus;
- ♦ Short-circuit protection;
- ◆ Thermal shutdown protection;
- ◆ Low-Current Shutdown Mode:
- ◆ Hot-Swap Input Structures on DE and /RE;
- ◆ True Fail-Safe Receiver:
- Excellent noise immunity;
- Integrated transient voltage suppression;
- ◆ 2.5Mbps in Electrically Noisy Environments:

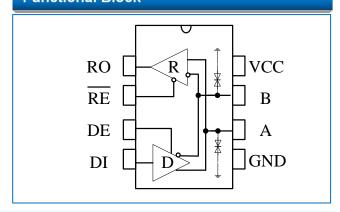
Applications

- ◆ Industrial control automation:
- Security system;
- instruments and meters:
- ◆ Road traffic control automation:
- ♦ Bas:
- Scene lighting control system;
- ◆ Level converter:

Configuration



Functional Block



General Description

UN485CUA is a 5V power supply,half-duplex, low power, high speed RS485/422 Transceiver. UN485CUA Fully meets the TIA/EIA-485 standard.

UN485CUA includes a driver and a receiver ,both of which can be independently enabled And disabled. When both are disabled, the driver and receiver outputs are high-impedance State. UN485CUA allows up to 32 Transceiver on the bus. The UN485CUA features reduced slew-rate drives that minimize EMI and reduce reflections caused by improperly terminated cables, allowing error-free date Transmission up to 2.5Mbps

UN485CUA operates under the supply voltage of 4.75V to 5.25V. UN485CUA is a true fail-safe Transceiver. UN485CUA also has the function of thermal shutdown protection, current limiting Protection, overvoltage protection, DE and /RE control port input features such as hot-swappable.



UN485CUA

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	VCC	+7	V
CTR Port	/RE, DE, DI	-0.3~VCC+0.3	V
Driver Output Voltage	A, B	-7~13	V
Receiver Output Voltage	RO	-0.3~VCC+0.3	V
Temperature Range		-40~85	$^{\circ}$
Storage Temperature Range		-60~150	${\mathbb C}$
Soldering Temperature(reflow)		300	$^{\circ}$
Continuous Power Dissipation		400	mW

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Pin Description

Pin Number	Pin Name	Function
1	RO	Receiver Output. When enabled, if $A-B \ge -10 \text{ mV}$, then $RO = \text{high}$. If $A-B \le -200 \text{ mV}$, then $RO = \text{low}$.
2	/RE	Receiver Output Enable. A low level enables the RO; a high level places it in a high impedance state.
3	DE	Driver Output Enable. A high level enables the driver differential outputs, Pin A and Pin B; a low level places the driver in a high impedance state.
4	DI	Driver Input. When the driver is enabled, a logic low on DI forces Pin A low and Pin B high; a logic high on DI forces Pin A high and Pin B low.
5	GND	Ground Connection (0 V).
6	A	No inverting Receiver Input A/Driver Output A.
7	В	Inverting Receiver Input B/Driver Output B.
8	VCC	Power Supply

UN Semiconductor Co., Ltd.



UN485CUA

DC Eelectrical Characteristics Of Driver

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Differential Driver Output(no load)	V_{OD1}			5		V
Diff. di ID i O 4 4	X.	Fig 2, RL = 27Ω	1.5		VCC	V
Differential Driver Output	$ m V_{OD2}$	Fig 2, $RL = 50 \Omega$	2		VCC	V
Change in Magnitude of Differential	$\Delta m V_{OD}$	Fig 2, RL = 27Ω			0.2	V
Output Voltage (NOTE1)	7 A OD	11g 2; KL = 27 32			0.2	•
Driver Common-Mode Output	V _{oc}	Fig 2, RL = 27Ω			3	V
Voltage	* 0C	119 27 102 27 32			3	v
Change In Magnitude of	$\Delta m V_{OC}$	Fig 2, $RL = 27 \Omega$			0.2	V
Common-Mode Voltage (NOTE1)	2,00	11g 2, RD 2/32			V. 2	•
Input High Voltage	$ m V_{IH}$	DE, DI, /RE	2.0			V
Input Low Voltage	$ m V_{IL}$	DE, DI, /RE			0.8	V
Input Current	I_{IN1}	DE, DI, /RE	-2		2	uA
Driver Short-Circuit Output	I_{OSD1}	Short to 0V~12V	35		250	mA
Current (short to high)	10801	Short to 0 V 12 V	33		230	ША
Driver Short-Circuit Output	$ m I_{OSD2}$	Short to -7V~0V	-250		-35	mA
Current (short to low)	108D2	Short to -7 v -0 v	-230		-55	ША
Thermal-Shutdown Threshold				150		$^{\circ}$
Thermal-Shutdown Hysteresis				20		$^{\circ}$

(If no special situation occurs, VCC=5V \pm 10%, Temp=TMIN~TMAX, typically VCC=+5V, Temp=25°C) NOTE1: \triangle V_{OD} and \triangle V_{OC} are the changes in V_{OD} and V_{OC}, respectively, when the DI input changes state.

3 / 10



UN485CUA

DC Eelectrical Characteristics Of Receiver

Parameter	Symbol	Conditions	Min	Тур	Max	Units
	$ m I_{IN2}$	$DE = 0 V,$ $VCC=0 \text{ or } 5V$ $V_{IN} = 12 V$			1.0	mA
Input Current (A, B)		$DE = 0 V,$ $VCC=0 \text{ or } 5V$ $V_{IN} = -7 V$	0.8			mA
Positive-going input threshold voltage	$V_{\rm IT+}$	$-7V \le V_{CM} \le 12V$			-10	mV
Negative-going input threshold voltage	V _{IT-}	$-7V \le V_{CM} \le 12V$	-200			mV
Receiver Input Hysteresis	Vhys	$-7V \le V_{CM} \le 12V$		30		mV
RO Output-High Voltage	V_{OH}	$I_{OUT} = -4mA,$ $V_{ID} = +200 \text{ mV}$	3.5			V
RO Output-Low Voltage	V_{OL}	$I_{OUT} = +4mA$, $V_{ID} = -200 \text{ mV}$			0.4	V
Three-State Output Current at Receiver	I_{OZR}	$0.4 \text{ V} < \text{V}_{\text{O}} < 2.4 \text{ V}$			±1	uA
Receiver Input Resistance	R _{IN}	$-7V \le V_{CM} \le 12V$	12			kΩ
Receiver Output Short-Circuit	I_{OSR}	0 V≤Vo≤VCC	±7		±95	mA

(If no special situation occurs, VCC=5V \pm 10% ,Temp=T_{MIN}~T_{MAX}, typically VCC=+5V, Temp=25 $^{\circ}$ C)

Supply Current

Parameter	Symbol	Conditions	Min	Тур	Max	Units
San Ja Carrest	I_{CC1}	/RE=0V or VCC, $DE=0V$		200	500	uA
Supply Current	I_{CC2}	/RE=VCC, DE=VCC		300	600	uA
Supply Current in Shutdown Mode	I_{SHDN}	/RE=VCC, DE=0V		0.5	10	uA



UN485CUA

Switching Characteristics Of Driver

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Driver Propagation Delay (low to high)	$t_{ m DPLH}$			60		ns
Driver Propagation Delay (high to low)	$t_{ m DPHL}$	$R_{DIFF} = 54 \Omega,$		60		ns
t _{DPLH} - t _{DPHL}	t _{skew1}	$C_{L1}=C_{L2}=100pF$ (Fig 3,4)		5	±10	ns
Driver Differential Output Rise or Fall Time	$t_{\mathrm{DR}},t_{\mathrm{DF}}$			40		ns
Driver Enable to Output High	$t_{\rm DZH}$	$C_L = 100 \text{ pF},$			70	ns
Driver Enable to Output Low	$t_{ m DZL}$	S1 closed (Fig 5,6)			70	ns
Driver Disable Time from Low	t _{DLZ}	$C_L = 15 \text{ pF},$			70	ns
Driver Disable Time from High	t _{DHZ}	S2 closed (Fig 5,6)			70	ns

Switching Characteristics Of Receiver

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Receiver Propagation Delay (low to high)	$t_{ m RPLH}$	Fig 7 and 8		50	200	ns
Receiver Propagation Delay (high to low)	t _{rphl}	V _{ID} ≥2.0V; Rise and fall time		50	200	ns
t _{rplh} - t _{rphl}	T_{skew2}	V _{ID} ≤15ns		13	15	ns
Receiver Enable to Output Low	t _{RZL}	CL = 100 pF, S1 closed (Fig 9,10)		20	50	ns
Receiver Enable to Output High	t _{RZH}	CL = 100 pF, S2 closed (Fig 9,10)		20	50	ns
Receiver Disable Time from Low	t_{RLZ}	$C_L = 100 \text{ pF},$ $S1 \text{ closed}$ (Fig 9,10)		20	50	ns
Receiver Disable Time from High	t _{RHZ}	$C_L = 100 \text{ pF},$ S2 closed (Fig 9,10)		20	50	ns

UN Semiconductor Co., Ltd.

www.unsemi.com.tw



UN485CUA

Function Tables

Transmitting

CI	R	Inputs	Outputs		
/RE	DE	DI	A	В	
X	1	1	Н	L	
X	1	0	L	Н	
0	0	X	Z	Z	
1	0	X	Z(shutudown)		
X: Don't care; Z: high impedance.					

Receiving

C	ΓR	Inputs	Outputs			
/RE	DE	A-B	RO			
0	X	≥-10mV	Н			
0	X	≤-200mV	L			
0	X	Open/shorted	Н			
1	X	X	Z			
X	X: Don't care; Z: high impedance					



UN485CUA

Test Circuit

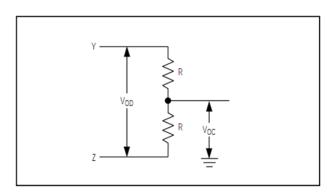
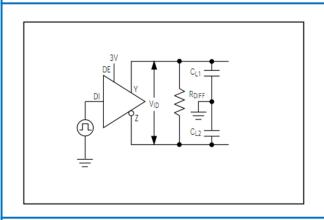


Fig 2 Driver DC Test Load



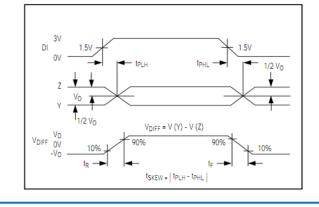


Fig 3 Driver Timing Test Circuit

OUTPUT UNDER TEST + CL S2 =

Fig 4 Driver Propagation Delays

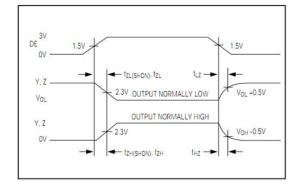


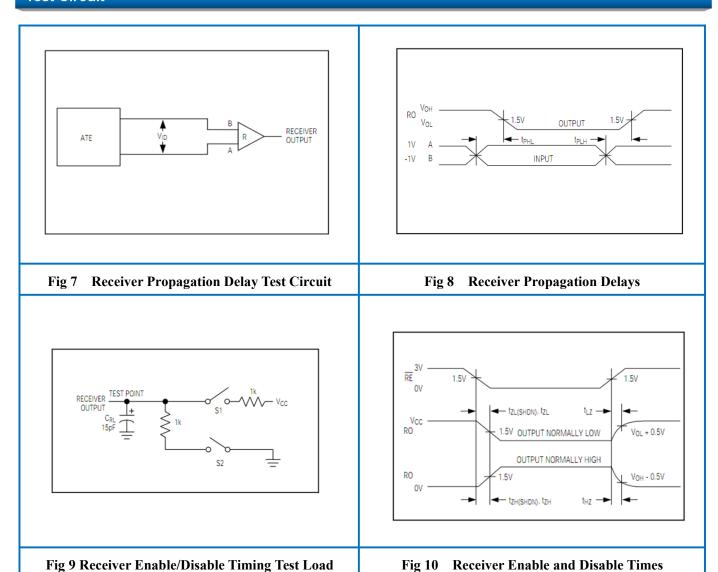
Fig 5 Driver Enable/Disable Timing Test Load

Fig 6 Driver Enable and Disable Times



UN485CUA

Test Circuit





UN485CUA

Summary

1 Description

UN485CUA, including a driver and a receiver, is a half-duplex high-speed transceivers for RS-485 / RS-422 communications. UN485CUA features fail-safe, overvoltage protection, overcurrent protection, thermal protection, and allows / RE, DE hot-swappable.

The UN485CUA allows an error-free data transmission up to 2.5Mbps.

2 Fail Safe

When the receiver inputs are shorted or open, or when they are connected to a terminated transmission line with all drivers disabled, UN485CUA guarantees a logic-high receiver output. This is done by the receiver input thresholds are set between -10mV and -200mV. If the differential receiver input voltage (A-B) \geq -10mV, RO is logic high; if the voltage (A-B) \leq -200mV, RO is logic low. When attached to the terminal all transmitters on the bus are disabled, the receiver differential input voltage is pulled to 0V by the termination resistor. With the receiver threshold of the UN485CUA, this results in a logic-high with a 10mV minimum noise margin. The -10mV to -200mV threshold complies with the \pm 200mV EIA/TIA-485 standard.

3 Connecting 32 Transceivers on one Bus

The standard UN485CUA receiver input impedance is guaranteed >12k Ω , the standard driver can drive up to 32 unit loads. These devices can be any combination, or in combination with other RS-485 transceiver combination, as long as the total load does not load more than 32 units, can be connected on the same bus.

4 Drive Output Protection

Through two mechanisms to avoid failure or a bus contention causes power consumption is too high. First, in the entire common Mode voltage range, overcurrent protection circuit provides a fast short protection. Second, when the die temperature exceeds 150°C, driver output is forced into a high impedance state by the thermal shutdown circuit.

5 Typical Application

UN485CUA transceiver is designed for multi-point bi-directional data communication bus transmission lines. Figure 11 shows a typical network application circuit. These devices can also be used as a cable longer than 4,000 feet of line repeater, to reduce the reflection, the transmission line should be in its ends terminated in its characteristic impedance, and stub lengths off the main line should be as short as possible.

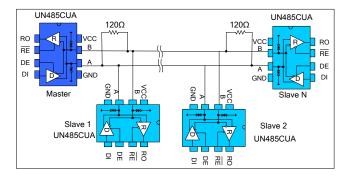
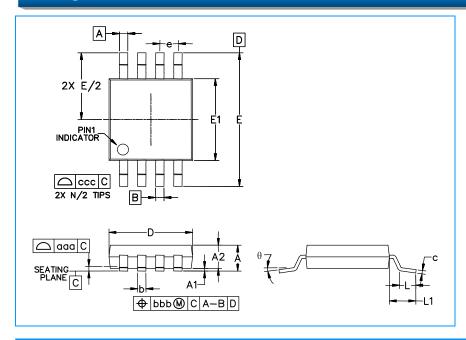


Fig11 Backbone cable type RS485 communications network



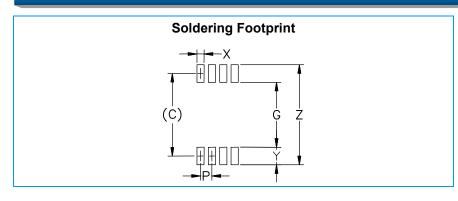
UN485CUA

Package Outline



Cumbal	Inches			Mi	llimete	ers
Symbol	Min.	Nom.	Max.	Min.	Nom.	Max.
Α	-	-	0.043	1	-	1.10
A1	0.000	-	0.004	0.00	-	0.10
A2	0.030	0.033	0.037	0.75	0.85	0.95
b	0.009	0.012	0.015	0.22	0.30	0.38
С	0.005	0.006	0.009	0.13	0.15	0.23
D	0.	.118 BS	С	3	.00 BS	С
E1	0.118 BSC			3.00 BSC		
E	0.	193 BS	C	4.90 BSC		
е	0.	026 BS	C	0.65 BSC		
L	0.016	0.021	0.026	0.40	0.53	0.66
L1	0.	037 RE	F	0	.95 RE	F
N		8			8	
θ	0°	1	8°	0° - 8°		
aaa	0.004				0.10	
bbb	0.003				0.08	
ccc		0.010			0.25	

Pad size



Symbol	Inches	Millimeters
С	0.1890	4.80
G	0.1448	3.78
Р	0.026	0.65
Х	0.0161	0.41
Υ	0.0402	1.02
Z	0.2291	5.82

Part	Temp Range	Pin-Package	Quantity
UN485CUA	-40℃~85℃	MSOP-8	2500pcs

Revision October 15, 2016