MDL14 Series

High Efficiency Step Down LED Driver

Features

- RoHS-compliant 8 Pin DIL Package
- Constant Current Output (±8% Output Current Accuracy)
- LED Driver Current 300/350/500/600/700/1000 mA
- Power LED Driver
- Wide Input Voltage Range: 7V to 16V (20V for 0.5sec.)
- Output Power 4.2 / 4.9 / 7 / 8.4 / 9.8 / 14 W
- Driver LED Strings of up to 14V (2V to 14V)
- High Efficiency (up to 93%)
- PWM/Digital Dimming and Analog Voltage Dimming
- Open and Short LED Protection
- -40°C ~ 85°C Operation Temperature Range
- With MLCC Capacitors only



- Suitable for high illumination LED
- Power limited (battery) lighting system

Perf. Criteria A

Perf. Criteria A

DL14 Series is a high efficiency step-down converter optimized to drive high current LEDs. The control algorithm allows highly efficient and accurate LED current regulation. The device operates from an input 7Vdc to 16Vdc and provides an externally adjustable output current of up to 1000mA and output power up to 14 watts. Compact size of DIL8 allows designer to integrate this driver together with LED module. UL 94V-0 grade molded case with high grade filling material provide excellent fire proof characters.

(Typical at Ta = +25°C, nominal input voltage, rated output current unless otherwise specified.

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ELECTRICAL SPECIFICATIONS:		DIMMING CONTROL AND ON/OFF CON	TROL (Leave Open if NotUsed):
Input Voltage (Vdc)	7V ~ 16V,12Vdc Nominal	VADJ Pin Input Voltage Range	0V to 1.25V
Input Filter	Capacitor	VADJ Pin Drive Current (VADJ = 1.25V)	<1mA
Output Voltage Range (Vin = 16V)	2V to 14V	Analog Dimming	
Output Current Range (Vin - Vout > 2V to 3V) See table	Adjust Output Current (Vin - Vout < 10V)	25% to 100%
Output Current Accuracy	See table	Control Voltage Range Limits	
Output Power	See table	On	0.3V < VADJ < 1.25V
Ripple and Noise, (20 MHz bandwidth)	See table	Off	Vadj < 0.15V
Maximum Efficiency at Full Load	93%	PWM Dimming	
Capacitive Load	47uF	Recommended Maximum Operation	
Operating Frequency	60 kHz ~400 kHz	Adjust Output Current	0% to 100%
Short Circuit Protection Regul	ated at Rated Output Current	Remote ON/OFF	
Temperature Coefficient	±0.03%/°C, max.		< VADJ < 1.25V or open circuit
Thermal Impedance (Nature Convection)	+35°C/W		5V or Short circuit pin 7 and pin 8
Safety Standard : (designed to meet)	IEC / EN 60950-1	Quiescent Input Currentin Shutdown M	ode (Vin = 16V) 25μ A, max.
ENVIRONMENTAL SPECIFICATION	IS	EMC SPECIFICATIONS	
Operating Temperature Range -4	0°C to +85°C(See Derating Curve)	EMI Radiated & Conducted Emissions	EN 55015 (CISPR22)
Storage Temperature Range	-40°C to +125°C	EMS Immunity EN61547	
Humidity	95% rel H	IEC 61000-4-2	Perf. Criteria A
Maximum Case Tempeature	+105°C	IEC 61000-4-3	Perf. Criteria A
Cooling	Nature Convection	IEC 61000-4-4	Perf. Criteria A

IEC 61000-4-6

IEC 61000-4-8

Soldering Temperature (1.5mm from case 10 sec. max.) +260°C, ma				
PHYSICAL SPEC	IFICATIONS			
Case Material	Non-Conductive Black PI	astic(UL94V-0 rated)		
Potting Material	Sil	icon (UL94V-0 rated)		
Pin Material	Ø0.5mm Bras	ss Solder-coated		
Weight		1.8g		
Dimensions	0	.80"x0.40"x0.27"		

NOTE

1. Reversed power source damages the circuit, No connection is allowed between input ground and output.

2.DO NOT operate the driverover output power.

Reliability Calculated MTBF(MIL-HDBK-217F)

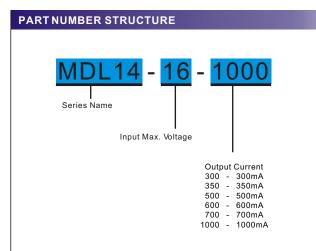
- 3.Leave pin VADJ open if notin use, ground pinto shut down the converter. Connecting VADJ to Vin damages the circuit.
- 4. Maximum output open voltage is equal to input voltage.
- 5. Input filter components (C1, C2, L, C3) are used to help meet conducted emissions requirement for the module.
- 6.For the compliance with IEC61000-4-5, a TVS is thus recommended to be installed in from of the input filter
- the reference model : 3.0SMCJ15A or SMCJ16A(TVS Max Clamping Voltage @ Max Peak Pulse Current VC (V) \leq 20V)

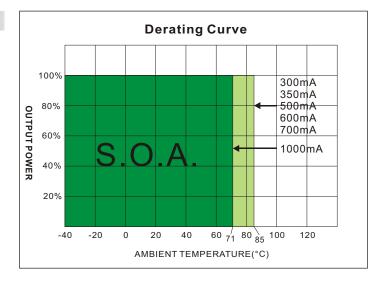
>3.3 Mhrs

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MODEL SELECTION GUIDE

	INPUT	OUTPU	Т	OUTPUT Current	OUTPUT			
MODEL NUMBER	Vol tage Rang e	Vol tage Ran ge	Current	Accuracy	Power	EFFICIE NCY	Ripple and Noise	Capacitor
	(Vdc)	(Vdc)	(mA)	(%)	(W) Max.	@FL(%) Max.	mVp-p Max.	Lo <i>a</i> d(uF)
MDL14-16-300	7-16	2 ~14	300	±5	4.2	93	120	47
MDL14-16-350	7-16	2~14	350	±6	4.9	93	150	47
MDL14-16-500	7-16	2 ~14	500	±7	7.0	93	200	47
MDL14-16-600	7-16	2~14	600	±7	8.4	93	200	47
MDL14-16-700	7-16	2~14	700	±7	9.8	93	250	47
MDL14-16-1000	7-16	2 ~14	1000	±8	14.0	93	250	47

EMC COUNTERMEASURES

EMC Countermeasures Suggestion

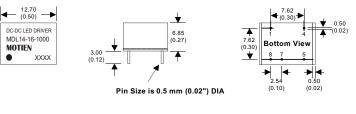
Input filter components (C1, C2, L, C3) are used to help meet conducted emissions requirement for the module.

These components should be mounted as close as possible to the module; and all leads should be minimized to decrease radiated noise.

For the compliance with IEC61000-4-5, a TVS is thus recommended to be installed in from of the input filter.

MECHANICAL DIMENSION

(0 40



+VIN

-VIN,

т

8 Pin DIL Package

VADJ

Analogue Dim PWM Dim/ON/OFF

47μH L

47µF

Сз

10uF

C2

10µF

C1

1210,106M/35V,X5R

Notes : All dimensions are typical in millimeters (inches). 1. Pin diameter: 0.5±0.05 (0.02±0.002) 2. Pin pitch and length tolerance: ±0.35 (±0.014) 3. Case Tolerance: ±0.5 (±0.02)

MDL14-16-XXX

+Vout

4

- Vout

1st LED

★ Last LED

Pin #	CONNECTIONS		
1	+V Input	+DC Supply	
4	+V Output	LED Anode Connection	
5	- V Output	LED Cathode Connection	
7	VADJ	Dimming Control	
8	- V Input	- DC Supply	

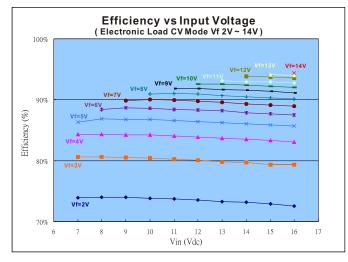
No connection is allowed between input and output

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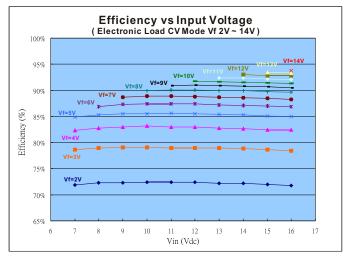


TYPICAL OPERATING CONDITIONS

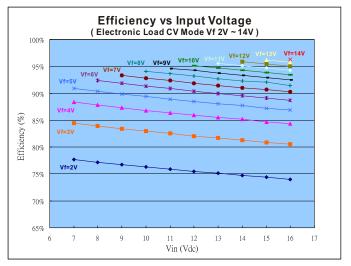
MDL14-16-300



MDL14-16-350

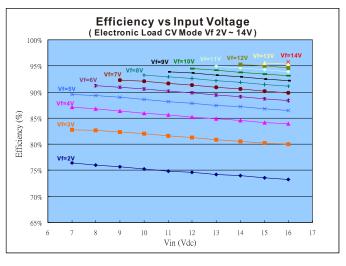


MDL14-16-500

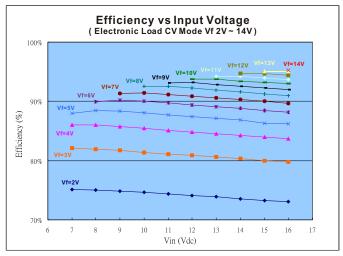


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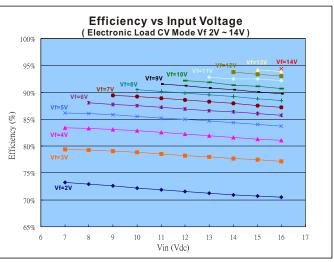
MDL14-16-600



MDL14-16-700



MDL14-16-1000

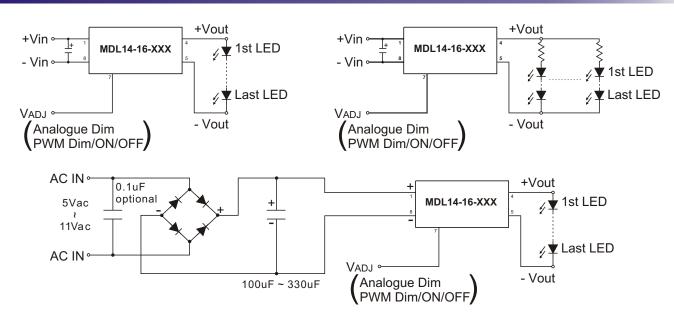


DRAWING: APPROVED Last Update : Feb.10.2012

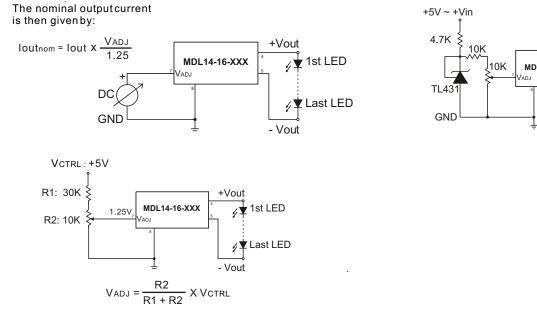
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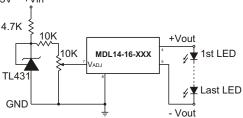


TYPICAL APPLICATION



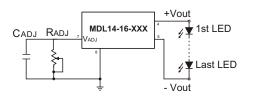
Output Current Adjustment By External DC Control Voltage





Resistor dimming

By connecting a variable resistor between ADJ and GND, simple dimming can be achieved. Capacitor CADJ is optional for better AC mains interference and HF noise rejection. Recommend value of CADJ is 0.22uF.



The current output loutnom can be determined using the equation:

$$\mathsf{lout_nom} = \frac{\mathsf{lout} X \mathsf{R} \mathsf{A} \mathsf{D} \mathsf{J}}{(\mathsf{R} \mathsf{A} \mathsf{D} \mathsf{J} + 200\mathsf{K})}$$

If the value of RADJ is 0 to 2M ohm, the maximum adjust range of output current is 25% to 90%. (For Vin-Vout<10Vdc)

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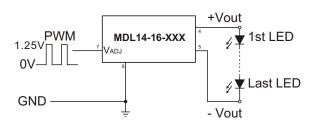


TYPICAL APPLICATION

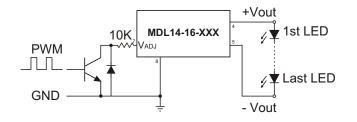
Output Current Adjustment By PWM Control

Directly driving ADJ input

A Pulse Width Modulated (PWM) signal with duty cycle, DPWM, can be applied to the ADJ pin, as shown below **Driving the ADJ input via open collector transistor** The diode and resistor suppress possible high amplitude negative spikes on the ADJ input resulting from the drain-s ource capacitance of the transistor. Negative spikes at the input to the device should be avoided as they may cause errors in output current, or erratic device operation.

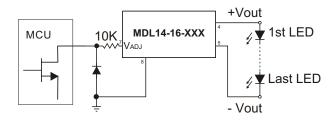


Ioutnom ≈ Iout x DPWM [If PWM frequency <200Hz, for 0.1<DPWM<1]



Driving the ADJ input from a microcontroller

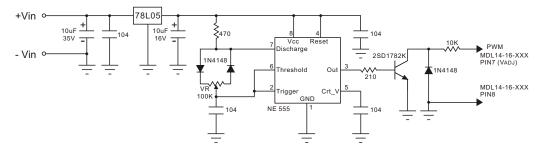
Another possibility is to drive the device from the open drain output of a microcontroller. The diagram below shows one method of doing this:



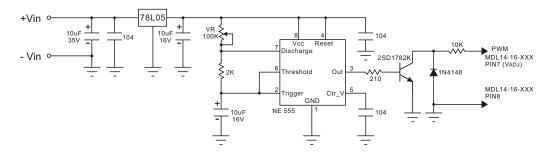
The diode and resistor suppress possible high amplitude negative spikes on the ADJ input resulting from the drain-s ource capacitance of the FET. Negative spikes at the input to the device should be avoided as they may cause errors in output current, or erratic device operation.

Output Current Adjustment By PWM Control (Dimming)

To avoid visible flicker the PWM signal must be greater than 100Hz.



Output Current Adjustment By PWM Control (Flash)



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