



FEATURES

- Single or Dual Isolated Outputs
- 1kVDC or 3kVDC options
- Wide temperature performance at full 0.75W load -40 C to 85C°
- Industry Standard Pinouts
- 5V, 12V and 24V Inputs
- 5V, 12V and 15V Outputs
- Pin compatible with NMR, MER1, MMV, MEV1, NMK, MEV3, NMV series.

DESCRIPTION

The CMR series are a cost effective 0.75W DC/DC converter series, in an industry standard package with industry standard pinouts., Popular input and output voltages are available as a lower power alternative to a 1W DC/DC converter. The galvanic isolation allows the device to be configured to provide an isolated negative rail in systems where only positive rails exist. The wide temperature range guarantees startup from -40° C and full 0.75 watt output at 85°C.

SELECTION GUI	IDE											
Order Code	Nominal Input Voltage	Output Voltage	Output Current	Load	Regulation	Rinnla &	Noise	Input Current at rated load		Efficiency	Isolation Capacitance	MTTF
	V	٧	mA	Тур.	Max	m\ Typ.	V p-p Max.	mA	Min.	% Typ.	pF	kHrs
CMR100C	5	5	150	9	11	6	10	218	65	69	30	1850
CMR118C	24	5	150	6.8	10	8	15	46	65	70	60	1250
				3K\	/DC Is	olation	Options					
CMR0505SA3C	5	5	150	9	11	15	25	220	64	68	30	4240
CMR0512S3C	5	±12	±31	5	6	6.7	8	192	74	78	30	1560
CMR0515S3C	5	±15	±25	4	6	6.3	8.2	188	76	79	30	1060
CMR1215S3C	12	±15	±25	3	4.5	6.5	13	80	76	79	40	925

INPUT CHARACTERIST	CS				
Parameter	Conditions	Min.	Тур.	Max.	Units
	Continuous operation, 5V input types	4.5	5	5.5	
Voltage range	Continuous operation, 12V input types	10.8	12	13.2	V
	Continuous operation, 24V input types	21.6	24	26.4	
Reflected ripple current	5V & 12V input types		2.6	4	mA p-p
nellected lipple current	24V input types		10	15	ша р-р

OUTPUT CHARACTERIST	TICS				
Parameter	Conditions	Min.	Тур.	Max.	Units
Rated Power ²	T _A =-40°C to 85°C			0.75	W
Voltage Set Point Accuracy	See tolerance envelope				
Line regulation	High V _{IN} to low V _{IN}		1.0	1.2	%/%

ISOLATION CHARACTER	ISTICS				
Parameter	Conditions	Min.	Тур.	Max.	Units
loolation voltage	C versions flash tested for 1 second	1000			VDC
Isolation voltage	3C versions flash tested for 1 minute	3000			VDC
Resistance	Viso=1000VDC	10			GΩ

ABSOLUTE MAXIMUM RATINGS		
Lead temperature 1.5mm from case for 10 seconds	260°C	
5V input types	7V	
12V input types	15V	
24V input types	28V	

- 1. Calculated using MIL-HDBK-217FN2 with nominal input voltage at full load.
- 2. See derating graph.
- 3. See ripple & noise characterisation method.

All specifications typical at Ta=25°C, nominal input voltage and rated output current unless otherwise specified.





CMR Series

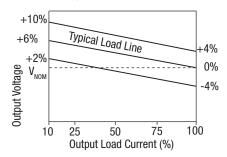
Isolated 0.75W Single and Dual Output Isolated DC/DC Converters

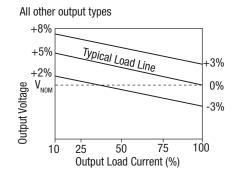
GENERAL CHARACTERISTIC	S				
Parameter	Conditions	Min.	Тур.	Max.	Units
	5V input types		110		
Switching frequency	12V input types		120		kHz
	24V input types		80		

TEMPERATURE CHARACTERIS	STICS				
Parameter	Conditions	Min.	Тур.	Max.	Units
Specification	All output types	-40		85	
Storage		-50		125	°C
Casa Tamparatura abaya ambiant	5V output types		33		
Case Temperature above ambient	All other output types		28		
Cooling	Free air convection				

TOLERANCE ENVELOPES

All 5Voutput types





The voltage tolerance envelope shows typical load regulation characteristics for this product series. The tolerance envelope is the maximum output voltage variation due to changes in output loading.

TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions CMR series of DC/DC converters are all 100% production tested at their stated isolation voltage. This is 1kVDC for 1 second for C versions and 3kVDC for 1 minute for 3C versions.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

For a part holding no specific agency approvals, such as the CMR series, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The CMR series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enameled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognized parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

Rohs Compliance Information



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. The pin termination finish on this product series is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The series is backward compatible with Sn/Pb soldering systems.

For further information, please visit www.murata-ps.com/rohs

www.murata-ps.com/support



APPLICATION NOTES

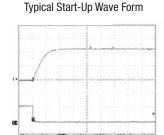
Minimum load

The minimum load to meet datasheet specification is 10% of the full rated load across the specified input voltage range. Lower than 10% minimum loading will result in an increase in output voltage, which may rise to typically double the specified output voltage if the output load falls to less than 5%.

Capacitive loading and start up

Typical start up times for this series, with a typical input voltage rise time of 2.2 μ s and output capacitance of 10 μ F, are shown in the table below. The product series will start into a capacitance of 47 μ F with an increased start time, however, the maximum recommended output capacitance is 10 μ F.

	Start-up time
	μs
CMR100C	2300
CMR118C	670
CMR0505SA3C	1970
CMR0512S3C	11200
CMR0515S3C	16300
CMR1215S3C	11200

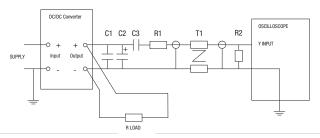


Ripple & Noise Characterisation Method

Ripple and noise measurements are performed with the following test configuration.

C1	1μF X7R multilayer ceramic capacitor, voltage rating to be a minimum of 3 times the output voltage of the DC/DC converter
C2	$10\mu F$ tantalum capacitor, voltage rating to be a minimum of 1.5 times the output voltage of the DC/DC converter with an ESR of less than $100m\Omega$ at $100~kHz$
C3	100nF multilayer ceramic capacitor, general purpose
R1	450Ω resistor, carbon film, $\pm 1\%$ tolerance
R2	50Ω BNC termination
T1	3T of the coax cable through a ferrite toroid
RLOAD	Resistive load to the maximum power rating of the DC/DC converter. Connections should be made via twisted wires
Measured value	es are multiplied by 10 to obtain the specified values.

Differential Mode Noise Test Schematic



APPLICATION NOTES (continued)

Output Ripple Reduction

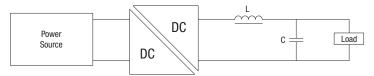
By using the values of inductance and capacitance stated, the output ripple at the rated load is lowered to 5mV p-p max.

Component selection

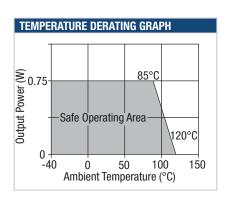
Capacitor: It is required that the ESR (Equivalent Series Resistance) should be as low as possible, ceramic types are recommended.

The voltage rating should be at least twice (except for 15V output), the rated output voltage of the DC/DC converter.

Inductor: The rated current of the inductor should not be less than that of the output of the DC/DC converter. At the rated current, the DC resistance of the inductor should be such that the voltage drop across the inductor is <2% of the rated voltage of the DC/DC converter. The SRF (Self Resonant Frequency) should be >20MHz

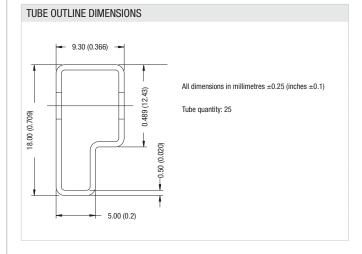


		Inductor		Capacitor
	L, μH	SMD	Through Hole	C, µF
CMR100C	10	82103C	11R103C	4.7
CMR118C	10	82103C	11R103C	4.7
CMR0505SA3C	22	82223C	11R223C	1
CMR0512S3C	150	82154C	11R154C	0.33
CMR0515S3C	220	82224C	11R224C	0.33
CMR1215S3C	220	82224C	11R224C	0.22

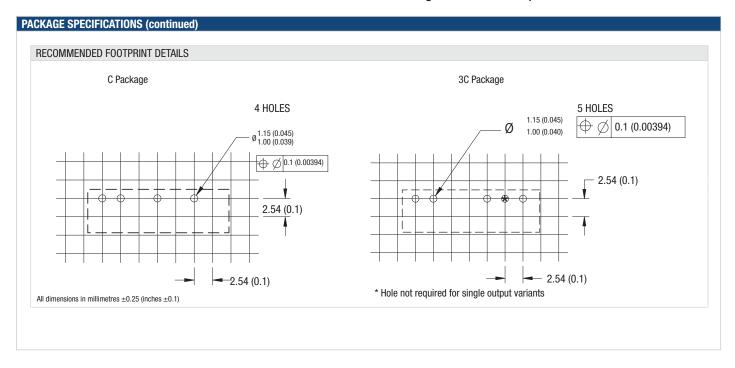


PACKAGE SPECIFICATIONS MECHANICAL DIMENSIONS C Package 3C Package 19.50 (0.77) 19.50 (0.77) 6.00 (0.236) 6.00 (0.236) muRata Ps muRata Ps 10.00 0.55 (0.0216) 0.45 (0.0177) 10.00 CMR0505S3C (0.394)(0.394)**CMR100C** __0.55 (0.022) 0.45 (0.0178) XYYWWi XYYWWi 2 5 6* 4 6 4.60 (0.181) 3.60 (0.142) _0.55 (0.0216) _0.45 (0.0177) 4.60 (0.181) 3.60 (0.141) -2.54 (0.1) 2.54 (0.1) 1.88±0.50 (0.074±0.02) -1.88±0.50 (0.074±0.02) 0.30 (0.0118) 0.20 (0.0079) _0.30 (0.0118) 0.20 (0.0079) -12.70 (0.5)-- 1.25 (0.049) 15.24 (0.6) All dimensions in millimetres ± 0.01 (inches ± 0.25). All pins on a 2.54 (0.1) pitch and within ± 0.25 (0.01) of true position. *PIN 6 not fitted on single Weight: 2.1g output variants

PIN CONNECTIONS



PIN	С	3C			
FIIN		Single	Dual		
1	+V _{IN}	+Vin	$+V_{\text{IN}}$		
2	-Vin	-Vin	-V _{IN}		
4	-Vоит				
5		-Vоит	-Vout		
6	+V 0UT		OV		
7		+Vоит	+V out		



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Refer to: http://www.murata-ps.com/requirements/

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